L Number	Hits	Search Text	DB	Time stamp
-	19	(interface same (match matching)) and ((non\$1 volatile eeprom eprom)	USPAT	2003/06/20 09:20
	_	same comparator same (controller (control adj1 circuit)))	TIOD AT	2002/06/10 12 16
-	8	(serial adj1 bus) and ((non\$1volatile eeprom eprom) same comparator	USPAT	2003/06/18 13:46
	277	same (controller (control adj1 circuit))) (non\$1volatile eeprom eprom) same comparator same (controller (control	USPAT	2003/06/18 12:20
-	277	adj1 circuit))	USFAI	2003/00/18 12.20
_	136	interface and ((non\$1 volatile eeprom eprom) same comparator same	USPAT	2003/06/18 10:17
-	150	(controller (control adj1 circuit)))	001111	
-	549	(flash eeprom eprom) same comparator same (controller (control adj1	USPAT	2003/06/18 12:24
		circuit))		
-	39	interface same ((flash eeprom eprom) same comparator same (controller	USPAT	2003/06/18 12:10
	_	(control adj1 circuit)))	**************************************	2002/06/18 12 22
•	5	(serial adj2 bus) and ((flash eeprom eprom) same comparator same	USPAT	2003/06/18 12:22
	8355	(controller (control adj1 circuit))) (flash eeprom eprom) and comparator and (controller (control adj1	USPAT	2003/06/18 12:24
-	6333	circuit))	031711	2003/00/10 12:21
_	476	(interface same (match matching)) and ((flash eeprom eprom) and	USPAT	2003/06/20 10:28
İ		comparator and (controller (control adj 1 circuit)))		
-	33415	(comparator comparison compar\$3 match\$3) near9 ((identification adj2	USPAT	2003/06/18 14:39
		data) address\$2 tag\$1)		0000/05/100
-	22078	((identification adj2 data) address\$2 tag\$1) near9 (non\$1 volatile flash	USPAT	2003/06/18 16:33
		rom eeprom eprom)	LICDATE	2002/06/19 14:41
-	1620	((comparator comparison compar\$3 match\$3) near9 ((identification adj2	USPAT	2003/06/18 14:41
		data) address\$2 tag\$1)) same (((identification adj2 data) address\$2 tag\$1) near9 (non\$1 volatile flash rom eeprom eprom))	:	
_	4	(serial adj2 bus) same (((comparator comparison compar\$3 match\$3)	USPAT	2903/06/18 14:42
•	7	near9 ((identification adj2 data) address\$2 tag\$1)) same (((identification		
		adj2 data) address\$2 tag\$1) near9 (non\$1volatile flash rom eeprom		
		eprom)))		
-	421715	(serial adj2 bus) (controller (control adj1 circuit))	USPAT	2003/06/18 14:44
-	1117	(((comparator comparison compar\$3 match\$3) near9 ((identification adj2	USPAT	2003/06/18 14:44
		data) address\$2 tag\$1)) same (((identification adj2 data) address\$2 tag\$1)		
		near9 (non\$1volatile flash rom eeprom eprom))) and ((serial adj2 bus) (controller (control adj1 circuit)))		
_	287	(((comparator comparison compar\$3 match\$3) near9 ((identification adj2	USPAT	2003/06/19 11:06
	207	data) address\$2 tag\$1)) same (((identification adj2 data) address\$2 tag\$1)		
		near9 (non\$1 volatile flash rom eeprom eprom))) same ((serial adj2 bus)		
		(controller (control adj1 circuit)))		
-	1	5293424.pn.	USPAT	2003/06/18 16:31
-	0	5293424.pn	USPAT	2003/06/18 16:30
-	1	((voltage adj3 detector) same (predetermined adj2 state)) same	USPAT	2003/06/19 09:57
	4	(non\$1 volatile flash rom eeprom eprom) ((voltage adj3 detector) same (predetermined adj2 state)) and	USPAT	2003/06/18 16:35
-	*	(non\$1 volatile flash rom eeprom eprom)	001711	2005/00/10 10:05
.	4	((voltage adj3 detector) same (predetermined adj2 state)) and	USPAT	2003/06/19 13:57
		(non\$1 volatile flash rom eeprom eprom)		
	69146	(reset near3 (signal input))	USPAT	2003/06/19 13:53
	54008	(reset adj2 (signal input))	USPAT	2003/06/19 13:44
-	262846	non\$1volatile flash rom eeprom eprom	USPAT	2003/06/19 13:44
-	3051	((reset adj2 (signal input))) same (non\$1 volatile flash rom eeprom eprom)	USPAT	2003/06/19 13:45 2003/06/19 13:47
•	291	((reset adj2 (signal input))) near4 (non\$1 volatile flash rom eeprom	USPAT	2003/06/19 13:47
_	0	eprom) (((reset adj2 (signal input))) near4 (non\$1volatile flash rom eeprom	USPAT	2003/06/19 13:48
•	U	eprom)) near10 advantag\$8	051711	2003/00/17 13:40
_	632	((reset adj2 (signal input))) near10 (non\$1 volatile flash rom eeprom	USPAT	2003/06/19 13:47
		eprom)		
-	1	(((reset adj2 (signal input))) near10 (non\$1 volatile flash rom eeprom	USPAT	2003/06/19 13:47
		eprom)) near10 advantag\$8		
-	75	(((reset adj2 (signal input))) same (non\$1 volatile flash rom eeprom	USPAT	2003/06/19 13:48
	22.00	eprom)) same advantag\$8	LICDAT	2002/06/10 12:55
_	2360 901	(reset near3 (signal input)) and (predetermined adj2 state) (non\$1volatile flash rom eeprom eprom) and ((reset near3 (signal input))	USPAT USPAT	2003/06/19 13:55 2003/06/19 13:55
-	901	and (predetermined adj2 state))	USFAI	2003/00/19 13.33
_	474	(reset near3 (signal input)) same (predetermined adj2 state)	USPAT	2003/06/19 13:56
	7/7	1 ((ordina mbas)) same (bradasamina nala amis)		

-	23	(non\$1 volatile flash rom eeprom eprom) same ((reset near3 (signal	USPAT	2003/06/19 14:19
	ļ	input)) same (predetermined adj2 state))		
-	551	(predetermined adj3 (input signal)) same (predetermined adj2 state)	USPAT	2003/06/19 13:59
-	1	((non\$1 volatile flash rom eeprom eprom) same ((reset near3 (signal	USPAT	2003/06/19 13:59
		input)) same (predetermined adj2 state))) and ((predetermined adj3 (input		
		signal)) same (predetermined adj2 state))		
-	58	(bonding near5 (wire pad)) near5 (non\$1 volatile flash rom eeprom	USPAT	2003/06/19 16:42
		eprom)		
-	845	(semiconductor adj2 chip) near10 (interface i/f cpu)	USPAT	2003/06/19 16:44
-	9	(semiconductor adj2 chip) near10 ((interface i/f) same cpu)	USPAT	2003/06/19 16:45
-	1775	(controller (control adj1 circuit) interface i/f compar\$3 match\$3) same	USPAT	2003/06/20 10:09
		((multi\$1chip multi-chip (multi adj1 chip)) mcp)		
-	12	(identification (predetermined adj2 state)) same ((controller (control adj1	USPAT	2003/06/20 09:24
		circuit) interface i/f compar\$3 match\$3) same ((multi\$1chip multi-chip		
		(multi adj1 chip)) mcp))		
-	7201	(multi\$1chip multi-chip (multi adj 1 chip))	USPAT	2003/06/20 09:27
-	1440	(controller (control adj1 circuit) and (interface i/f) and (compar\$3	USPAT	2003/06/20 09:29
		match\$3)) and ((multi\$1chip multi-chip (multi adj1 chip)))	LICDATE	2002/06/20 00 20
-	1345	(controller (control adj circuit) same (interface i/f) same (compar\$3	USPAT	2003/06/20 09:30
	510	match\$3)) and ((multi\$1chip multi-chip (multi adj1 chip)))	USPAT	2003/06/20 09:38
-	510	(controller (control adj l circuit) same (interface i/f) same (compar\$3	USPAI	2003/00/20 09:38
		match\$3)) same (predetermined adj2 state) ((controller (control adj1 circuit) same (interface i/f) same (compar\$3	USPAT	2003/06/20 09:32
-	1	match\$3)) same (predetermined adj2 state)) same identification	USFAI	2003/00/20 09.32
	121	((controller (control adj1 circuit) same (interface i/f) same (compar\$3	USPAT	2003/06/20 09:37
-	121	match\$3)) same (predetermined adj2 state)) and identification	OSIAI	2003/00/20 07.37
_	1	(((controller (control adj1 circuit) same (interface i/f) same (compar\$3	USPAT	2003/06/20 09:32
-		match\$3)) same (predetermined adj2 state)) and identification) and	051111	2003/00/20 03:32
		((multi\$1chip multi-chip (multi adj1 chip)))		
_	2614	(controller (control adj1 circuit) same (interface i/f) same (compar\$3	USPAT	2003/06/20 10:10
	2011	match\$3)) and (predetermined adj2 state)		
<u>-</u>	1	((controller (control adj1 circuit) same (interface i/f) same (compar\$3	USPAT	2003/06/20 09:38
	_	match\$3)) and (predetermined adj2 state)) and identification and		
		((multi\$1chip multi-chip (multi adj1 chip)))		
-	4	((controller (control adj1 circuit) same (interface i/f) same (compar\$3	USPAT	2003/06/20 09:40
		match\$3)) same (predetermined adj2 state)) and ((multi\$1chip multi-chip		
		(multi adj 1 chip)))		
-	14	(predetermined adj1 state) and ((multi\$1chip multi-chip (multi adj1	USPAT	2003/06/20 09:40
		(chip)))		
-	15	((controller (control adj1 circuit) same (interface i/f) same (compar\$3	USPAT	2003/06/20 10:10
		match\$3)) and (predetermined adj2 state)) and ((multi\$1chip multi-chip		
		(multi adj l chip)))		
-	8	(serial adj1 bus) and ((non\$1 volatile eeprom eprom) same comparator	USPAT	2003/06/20 10:26
		same (controller (control adj1 circuit)))		
-	476	(interface same (match matching)) and ((flash eeprom eprom) and	USPAT	2003/06/20 11:33
•		comparator and (controller (control adj1 circuit)))		